

**REMARKS**

New claims 32-34 have been added. Claims 1-34 are currently pending in the application. Reconsideration is respectfully requested.

Claims 1-21 were indicated as allowed. Claims 23-31 were objected to as being dependent on a rejected base claim. Applicants thank the Examiner for the indication of allowable subject matter.

Claim 23 has been amended to include the limitations of claim 22. Claim 23 is accordingly in condition for favorable action and allowance.

Claim 22 was rejected under 35 U.S.C. 102(b) as being anticipated by Davis. Applicants have amended claim 22 to emphasize that the second circuit detects whether the output signal oscillates at a frequency that is less than a predetermined frequency by determining whether the output signal remains in one of a first or second logic state in excess of a predetermined period of time. While the frequency to voltage converter (15) of Davis operates to determine whether the receives signals has a frequency that is less than a predetermined frequency, there is no teaching or suggestion in Davis for having that converter operate in the manner claimed by Applicants. More specifically, Applicants claim having the circuit determine whether the received signal remains in one of the first or second logic state for more that a predetermined period of time. Davis teaches that the converter 15 is a “standard” device. There is no showing, however, that such a “standard” device operates to make the first or second logic state time comparison determination claimed by Applicants. Accordingly, Applicants respectfully submit that claim 22 distinguishes over the cited art.

CUSTOMER NO. 30430

PATENT APPLICATION  
Docket No. 02-C-085

In view of the foregoing, Applicants respectfully submit that the application is in condition for favorable action and allowance.

Respectfully submitted,  
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